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## Pentium-Mmx Based Implementation Of A Digital Copier [\(Make Corrections\)](#)

Jae-Woo Ahn and Wonyong Sung School of Electrical Engineering,  
Seoul National ...

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**Abstract:** In this paper, we develop real-time image processing programs for a digital copier using a general purpose microprocessor. To exploit the inherent data parallelism in many image processing algorithms, we use the Intel's Pentium processor with multimedia-extension (MMX). Each step of the digital copier process including the X-Zoom and the error diffusion halftoning is aggressively optimized for the Pentium MMX processor. The X-Zoom process that is based on the linear interpolation method is... ([Update](#))

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0.3: Fast Stereo Matching for the VIDET System using a General.. - Di Stefano, Mattoccia (2000) ([Correct](#))

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0.2: A Block Priority Based Instruction Caching Scheme - For Multimedia Processors ([Correct](#))

**BibTeX entry:** ([Update](#))

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@misc{ and-pentiummmx,
  author = "Jae-Woo Ahn And",
  title = "Pentium-Mmx Based Implementation Of A Digital Copier",
  url = "citeseer.ist.psu.edu/294353.html" }
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64 A parallel algorithm for the efficient solution of a general.. (context) - Kogge, Stone - 1973

52 An adaptive algorithm for spatial greyscale (context) - Floyd, Steinberg - 1976

4 Pipelining in algorithms with quantizer loops (context) - Parhi - 1991

3 The Complete Guide to MMX Technology (context) - Corporation - 1997

1 Intel MMX Technology Overview (context) - Corporation - 1996

1 Implementation of digital filtering algorithms using pipelin.. (context) - Sung, Mitra - 1987

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IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

# 1. Mixed analog-digital fuzzy logic controller with continuous-amplitude fuzzy inference defuzzification

Bouras, S.; Kotronakis, M.; Suyama, K.; Tsividis, Y.

[Fuzzy Systems, IEEE Transactions on](#)

Volume: 6 Issue: 2 May 1998

Page(s): 205-215

Digital Object Identifier 10.1109/91.669017

**Summary:** A fuzzy logic controller has been realized using mixed analog-digital CMOI integration (VLSI) circuits for application in cases where the input and output variables form. It employs a new architecture where time sweepin.....

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#) IEEE JNL

# 2. Pentium-MMX-based implementation of a digital copier

Ahn, J.-W.; Sung, W.

[Signal Processing Systems, 1998. SIPS 98. 1998 IEEE Workshop on](#)

8-10 Oct 1998

Page(s): 142-151

Digital Object Identifier 10.1109/SIPS.1998.715777

**Summary:** We develop real-time image processing programs for a digital copier using purpose microprocessor. To exploit the inherent data parallelism in many image proce we use Intel's Pentium processor with multimedia extension (MMX).....

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